JP06-6175

PATENT ABSTRACTS OF JAPAN

(11)Publication number:

06-006175

(43) Date of publication of application: 14.01.1994

(51)Int.CI.

HO3H 9/56

H03H 9/02

(21)Application number : **04-161185**

(71)Applicant: MURATA MFG CO LTD

(22)Date of filing:

19.06.1992

(72)Inventor: IRIE MAKOTO

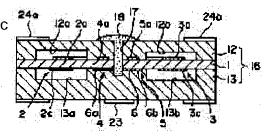
(54) PIEZOELECTRIC COMPONENT

(57)Abstract:

PURPOSE: To suppress stray capacitance generated between an input electrode and an output electrode and interference between vibrators.

CONSTITUTION: A vibrator section 2 and a capacitor section 4 are provided on the input side of a piezoelectric substrate 1 and a vibrator section 3 and a capacitor section 5 are provided on the output side of the piezoelectric substrate 1. Then the piezoelectric substrate 1 and sealing substrates 12, 13 form a laminator 16 provided with an enclosed vibration space. After a groove 17 completely separating the piezoelectric substrate 1 and the sealing substrate 12 is formed

among the vibrator section 2, the capacitor section 4, the



vibrator section 3, the capacitor section 5 in the laminator, the groove 17 is filled by a resin 18 whose dielectric constant is lower than that of the substrates 1, 12.

LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than

06-6175

the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office

* NOTICES *

06-6175

Japan Patent Office is not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original
- 2.*** shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

CLAIMS

[Claim(s)]

[Claim 1] A piezo electric crystal substrate which has the two vibrator sections and the two capacitor sections at least, prepared one [said] vibrator section and capacitor section in one substrate side, and prepared the vibrator section and the capacitor section of said another side in an another side side of a substrate, A slot which is equipped with two closure substrates which form oscillating space on both sides of said piezo electric crystal substrate, and divides said piezo electric crystal substrate and one [said] closure substrate, respectively One [said] vibrator section and capacitor section, Piezo-electric components characterized by having prepared between the vibrator section of said another side, and the capacitor section, and filling up this slot with resin.

[Claim 2] A piezo electric crystal substrate with the two vibrator sections and the one capacitor section arranged between these two vibrator sections at least, It has two closure substrates which form oscillating space on both sides of said piezo electric crystal substrate. Piezo-electric components characterized by having prepared a slot which divides said piezo electric crystal substrate and one [said] closure substrate, respectively between said capacitor sections and one [said] vibrator sections and between said capacitor sections and vibrator sections of said another side, and filling up this slot

[Claim 3] A piezo-electric component according to claim 1 or 2 characterized by a reinforcement member having joined to a closure substrate divided by slot where said slot is straddled at least. [Claim 4] A piezo electric crystal substrate which has at least two vibrator sections, and two closure substrates which form oscillating space on both sides of said piezo electric crystal substrate, A slot which is equipped with a dielectric substrate with at least one capacitor section, and divides said piezo electric crystal substrate and one [said] closure substrate, respectively Piezo-electric components characterized by preparing a slot which fills up this slot with resin while preparing between said two vibrator sections, and divides said dielectric substrate between an input side of said dielectric substrate, and an output side.

[Claim 5] A piezo-electric component according to claim 4 characterized by establishing a slot which divides a dielectric substrate even in a closure substrate which touches said dielectric substrate. [Claim 6] Claim 1 characterized by establishing a slot which divides one closure substrate and piezo electric crystal substrate, respectively even in a closure substrate of another side, claim 2, or a piezoelectric component according to claim 3, 4, or 5.

[Claim 7] A piezo electric crystal substrate with the two vibrator sections and the one capacitor section at least, It has a propagation-of-vibration prevention member joined to two closure substrates which form oscillating space on both sides of said piezo electric crystal substrate, and one [said] closure substrate through flexible adhesives. Piezo-electric components characterized by having prepared a slot which reaches even said propagation-of-vibration prevention member between said vibrator sections while dividing said two closure substrates and said piezo electric crystal substrate, respectively, and filling up this slot with resin.

[Translation done.]

06/6175

06+6175

* NOTICES *

Japan Patent Office is not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.**** shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

DETAILED DESCRIPTION

[Detailed Description of the Invention]

[0001]

[Industrial Application] This invention relates to the piezo-electric components used for an oscillator circuit, a filter circuit, etc.

[0002]

[Description of the Prior Art] Conventionally, as this kind of piezo-electric components, there is a thing of the laminating type shown in <u>drawing 18</u>. This piezo-electric component constitutes the layered product from a piezo electric crystal substrate 120 which has the capacitor section for combining the two vibrator sections and these two vibrator sections, and two closure substrates 121,122 which form oscillating space on both sides of this piezo electric crystal substrate 120. An input electrode 125 and an output electrode 126 are formed in the both ends of this layered product, and the grand electrode 127 is formed in the center section.

[0003] However, if this piezo-electric component was miniaturized further, since the gap of an input electrode 125 and an output electrode 126 would become narrow, big stray capacity occurred between the input electrode 125 and the output electrode 126. Moreover, apart from this, the problem that a RF property deteriorated also had vibration produced in one vibrator section by spreading the closure substrate 121,122 and interfering in the vibrator section of another side.

[0004] Then, the technical problem of this invention is to offer the piezo-electric components of the structure where the stray capacity generated between an input electrode and an output electrode and interference between the vibrator sections can be suppressed.

[0005]

[Means for Solving the Problem and its Function] In order to solve the above technical problem, piezo-electric components concerning this invention were equipped with a piezo electric crystal substrate which has the capacitor section for combining the two vibrator sections and these two vibrator sections, and two closure substrates which form oscillating space on both sides of this piezo electric crystal substrate, and prepared a slot which divides said piezo electric crystal substrate and one [said] closure substrate, respectively between said two vibrator sections. This slot is filled up, resin, for example, epoxy system resin etc., of a dielectric constant lower than a piezo electric crystal substrate and a closure substrate etc. Therefore, since a dielectric constant of a piezo electric crystal substrate which prepared a slot, and the whole closure substrate becomes small, stray capacity generated between I/O of piezo-electric components becomes small.

[0006] Moreover, if a reinforcement member is joined to a closure substrate divided by slot where said slot is straddled at least, a fall of a mechanical strength of piezo-electric components by having prepared a slot will be compensated. Moreover, a dielectric substrate may be joined to a layered product which consists of a piezo electric crystal substrate and a closure substrate, and capacity for combining the two vibrator sections may be secured in the capacitor section prepared in a dielectric substrate. In this case, stray capacity during I/O of piezo-electric components is stopped by preparing a slot which divides a dielectric substrate into a dielectric substrate.

[0007] Furthermore, if this division slot is established even in a closure substrate of another side when forming a slot which divides a piezo electric crystal substrate and one closure substrate in a layered product which consists of a piezo electric crystal substrate and a closure substrate, said piezo electric crystal substrate and one [said] closure substrate will be divided certainly. Moreover, a propagation-of-vibration prevention member is joined to a layered product which consists of a piezo electric crystal substrate and a closure substrate through flexible adhesives, for example, adhesives of a rubber system. If structure which prepares a slot which reaches even said propagation-of-vibration prevention member between the two vibrator sections, and fills up this slot with resin is adopted while dividing said piezo electric crystal substrate and said closure substrate Vibration produced in one vibrator section is interrupted by resin and flexible adhesives which are filled up with a slot, and does not need to affect the vibrator section of another side.

[8000]

[Example] Hereafter, the example of the piezo-electric components concerning this invention is explained with reference to an accompanying drawing. In each example, the same sign was given to the same components and the same portion.

The piezo-electric components of the 1st example of [the 1st example, drawing 1 - drawing 6] contain two piezoelectric transducers and two capacitors, form one piezoelectric transducer and capacitor of them in the input side of a piezo electric crystal substrate, and form other piezoelectric transducers and capacitors in the output side of a piezo electric crystal substrate. As shown in drawing 1, piezo-electric components consist of a piezo electric crystal substrate 1 and closure substrates 12 and 13 which form oscillating space on both sides of this piezo electric crystal substrate 1.

[0009] The piezo electric crystal substrate 1 consists of ceramic material, such as PZT. The input-side cash-drawer electrode 7 connected to vibrating electrode 2a, 2b, vibrating electrodes 3a and 3b, capacity electrode 4a, capacity electrode 5a, electrode 2b, and 3b, respectively and the output side cash-drawer electrode 8, and auxiliary electrodes 9a and 9b are formed in the surface of the piezo electric crystal substrate 1. Furthermore, as shown in drawing 2, the capacity electrode 6 which should finally be divided into capacity electrode 6a and capacity electrode 6b by vibrating electrode 2c, vibrating electrode 3c, and the below-mentioned division slot 17 is formed in the rear face of a substrate 1 at the position. The vibrator sections 2 and 3 consist of vibrating electrodes 2a-2c, and 3a-3c, respectively, and the capacitor sections 4 and 5 consist of capacity electrodes 4a, 6a, 5a, and 6b, respectively. [0010] The insulating closure substrates 12 and 13 are arranging the crevices 12a, 12b (refer to drawing 4), 13a, and 13b for oscillating space formation in one field. Next, the assembly procedure of the above components 1, 12, and 13 is explained. First, as shown in drawing 3, it considers as the layered product 16 which has the oscillating space of the piezo electric crystal substrate 1 which fixed the closure substrates 12 and 13 through the adhesives of an epoxy system etc. up and down, and was sealed. The slot 17 which divides the piezo electric crystal substrate 1 and the closure substrate 12 into two is formed in this layered product 16. The depth of a slot 17 is set up in order to divide completely the piezo electric crystal substrate 1 and the closure substrate 12 at this time, to take manufacture tolerance etc. into consideration and to establish a slot 17 even in the closure substrate 13. However, if the conditions of dividing substrates 1 and 12 completely are satisfied, it is not necessary to necessarily establish a slot 17 in a substrate 13. The capacity electrode 6 is divided by the slot 17 and set to capacity electrode 6a and capacity electrode 6b. Therefore, this slot 17 will be formed between the vibrator section 2 and the capacitor section 4, the vibrator section 3, and the capacitor section 5.

[0011] Next, a slot 17 is filled up with resin 18 as shown in <u>drawing 4</u>. It is used as a material of resin 18, the thing, for example, the epoxy system resin etc., of a dielectric constant lower than the piezo electric crystal substrate 1 and the closure substrates 12 and 13 etc. Resin 18 is formed in opening of a slot 17 in the condition that it was able to heap up in extensive area. It is for lessening the fall of the mechanical strength of the piezo-electric components by having formed the slot 17 by heaping up resin 18.

[0012] Next, as shown in <u>drawing 5</u>, while forming an input electrode 20, an output electrode 21, and auxiliary electrodes 24a and 24b in the both ends of a layered product 16 in thin film means forming,

such as a spatter and vacuum evaporationo, respectively, the grand electrode 23 is formed in the junction electrode 22 and a back side edge side at the near-side end face of a layered product 16. And an input electrode 20 is connected to the cash-drawer electrode 7, an output electrode 21 is connected to the cash-drawer electrode 8, the junction electrode 22 is connected to the capacity electrodes 4a and 5a, and the grand electrode 23 is connected to the capacity electrodes 6a and 6b. The electric representative circuit schematic of piezo-electric components is shown in drawing 6.

[0013] In this way, since the slot 17 filled up with resin 18 with a low dielectric constant is formed between the input electrode 20 and the output electrode 21, the dielectric constant between an input electrode 20 and an output electrode 21 becomes small, and the obtained piezo-electric components become what has the small stray capacity generated between the I/O electrodes 20-21.

The piezo-electric components of the 2nd example of [the 2nd example, drawing 7 - drawing 10] are equipped with two piezoelectric transducers and one capacitor. As shown in drawing 7, it consists of a piezo electric crystal substrate 31 and closure substrates 42 and 43 which form oscillating space on both sides of this piezo electric crystal substrate 31. The junction cash-drawer electrodes 34a and 34b connected to vibrating electrodes 32a and 32b, vibrating electrodes 33a and 33b, the input-side cash-drawer electrode 37 connected to Electrodes 32a and 33a, respectively, the output side cash-drawer electrode 38, and Electrodes 32b and 33b, respectively and capacity electrode 36a are prepared in the surface of the piezo electric crystal substrate 31. Furthermore, as shown in drawing 8, the gland side cash-drawer electrodes 35a and 35b connected to vibrating electrode 32c, vibrating electrode 33c, and Electrodes 32c and 33c, respectively and capacity electrode 36b are prepared in the rear face of a substrate 31 at the position. The vibrator sections 32 and 33 consist of vibrating electrodes 32a-32c, and 33a-33c, respectively, and the capacitor section 36 consists of capacity electrodes 36a and 36b. This piezo electric crystal substrate 1 will be divided by the two below-mentioned division slots 47a and 47b established in the both-sides portion of the capacitor section 36.

[0014] The insulating closure substrates 42 and 43 are arranging the crevices 42a, 42b (refer to drawing 9), 43a, and 43b for oscillating space formation in one field. As shown in drawing 9, let these components 31, 42, and 43 be the layered products 46 which are accumulated, fix and have the sealed oscillating space. Two slots 47a and 47b which trichotomize the piezo electric crystal substrate 31 and the closure substrate 42 into this layered product 46 are formed between the vibrator section 32 and the capacitor section 36 and between the vibrator section 33 and the capacitor section 36, respectively. It fills up with resin 48 with a low dielectric constant, and resin 48 can be heaping up opening of Slots 47a and 47b for these slots 47a and 47b in extensive area.

[0015] Next, as shown in <u>drawing 10</u>, while forming an input electrode 50, an output electrode 51, and auxiliary electrodes 54a and 54b in the both ends of a layered product 46 in thin film means forming, respectively, the grand electrode 53 is formed in the junction electrode 52 and a back side edge side at the near-side end face of a layered product 46. And an input electrode 50 is connected to the cash-drawer electrode 37, an output electrode 51 is connected to the cash-drawer electrode 38, the junction electrode 52 is connected to capacity electrode 36a and the junction electrodes 34a and 34b, and the grand electrode 53 is connected to capacity electrode 36b and the cash-drawer electrodes 35a and 35b. [0016] In this way, the obtained piezo-electric components do so the same operation as the piezo-electric components of said 1st example, and an effect.

As shown in the 3rd example and [drawing 11] drawing 11, the piezo-electric components of the 3rd example fix the reinforcement substrate 60 with adhesives, such as an epoxy system, to the layered product 16 which consists of a piezo electric crystal substrate 1 explained in said 1st example, and closure substrates 12 and 13. A slot 17 divides the piezo electric crystal substrate 1 and the closure substrate 12 into two, and is established even in the closure substrate 13. The slot 17 is filled up with resin 18 with a low dielectric constant. An input electrode 61 and an output electrode 62 are formed in the both ends of piezo-electric components, and the grand electrode 63 is formed in the center section. [0017] The piezo-electric components which consist of the above configuration have the structure where the fall of the mechanical strength of the piezo-electric components by having formed the slot 17 with the reinforcement substrate 60 can be compensated enough while doing so the same operation as the

piezo-electric components of said 1st example, and an effect.

The piezo-electric components of the 4th example of [the 4th example, drawing 12 - drawing 16] contain two piezoelectric transducers and one capacitor. As shown in drawing 12, it consists of a piezo electric crystal substrate 71, closure substrates 82 and 83 which form oscillating space on both sides of this piezo electric crystal substrate, and a dielectric substrate 88 (refer to drawing 14). The junction cash-drawer electrodes 77a and 77b and auxiliary electrodes 78a and 78b which were connected to the input-side cash-drawer electrode 75 connected to vibrating electrodes 72a and 72b, vibrating electrodes 73a and 73b, and Electrodes 72a and 73a, respectively and the output side cash-drawer electrode 76, and Electrodes 72b and 73b, respectively are prepared in the surface of the piezo electric crystal substrate 71. Furthermore, as shown in drawing 13, the gland side cash-drawer electrodes 79a and 79b connected to vibrating electrode 72c, vibrating electrode 73c, and Electrodes 72c and 73c, respectively are formed in the rear face of a substrate 71. The vibrator sections 72 and 73 consist of vibrating electrodes 72a-72c, and 73a-73c, respectively. This piezo electric crystal substrate 71 will be divided by the belowmentioned division slot 87 prepared between the vibrator section 72 and the vibrator section 73. As for the closure substrates 82 and 83, the crevices 82a, 82b (refer to drawing 16), 83a, and 83b for oscillating space formation are arranged in one field.

[0018] As shown in drawing 14, let these components 71, 82, and 83 be the layered products 85 which are accumulated, fix and have the sealed oscillating space. The slot 87 which divides the piezo electric crystal substrate 71 and the closure substrate 82 into two is formed in this layered product 85 between the vibrator section 72 and the vibrator section 73. This slot 87 is filled up with resin 88 with a low dielectric constant. The dielectric substrate 90 fixes through adhesives on the upper surface of this layered product 85. Capacity electrode 91a and the substrate electrodes 92a, 92b, 92c, and 92d are formed in the surface of the dielectric substrate 90. Capacity electrode 91b is prepared in the rear face of the dielectric substrate 90. The capacitor section 91 consists of capacity electrodes 91a and 91b. [0019] Next, as shown in drawing 15, while forming an input electrode 94, an output electrode 95, and auxiliary electrodes 98a and 98b in the both ends of piezo-electric components in thin film means forming, respectively, the grand electrode 97 is formed in the near-side end-face section of piezo-electric components at the junction electrode 96 and the back side end-face section. An input electrode 94 is connected to the input-side cash-drawer electrode 96 is connected to the junction cash-drawer electrodes 77a and 77b, and the grand electrode 97 is connected to the grand cash-drawer electrodes 79a and 79b.

[0020] Furthermore, in order to stop the stray capacity between an input electrode 94 and an output electrode 95, the slots 99a and 99b where width of face is wide are formed between an input electrode 94 and the grand electrode 97 and between an output electrode 95 and the grand electrode 97. These slots 99a and 99b take manufacture tolerance etc. into consideration, and are established even in the closure substrate 82 so that the dielectric substrate 90 may be trichotomized completely. However, if the conditions of dividing a substrate 90 completely are satisfied, it is not necessary to necessarily establish Slots 99a and 99b in a substrate 82. Moreover, only one slot of the slots 99a and 99b may be prepared. [0021] <u>Drawing 16</u> is the vertical cross section of the obtained piezo-electric components. Capacity occurs among the capacity electrodes 91a and 91b. This piezo-electric component does so the same operation as the piezo-electric components of said 1st example, and an effect.

As shown in the 5th example and [drawing 17] drawing 17, the piezo-electric components of the 5th example paste up the propagation-of-vibration prevention substrate 103 on the layered product 16 which consists of a piezo electric crystal substrate 1 explained in said 1st example, and closure substrates 12 and 13 with the flexible adhesives (adhesives which have flexibility) 102. The resin adhesives of a rubber system etc. are used as a material of the flexible adhesives 102. A slot 100 divides the piezo electric crystal substrate 1 and the closure substrates 12 and 13 into two, and is established even in the propagation-of-vibration prevention substrate 103. The slot 100 is filled up with resin 101 with a low dielectric constant. The input electrode 104 and the output electrode 105 are formed in the both ends of piezo-electric components.

[0022] In the above configuration, a part of vibration generated in the vibrator sections 2 and 3, respectively is spread to the closure substrates 12 and 13. However, vibration spread to the closure substrate 12 is interrupted with the resin 101 with which the slot 100 was filled up, and vibration spread to the closure substrate 13 is interrupted by the flexible adhesives 102. Therefore, the piezo-electric components which do not interfere each other in vibration generated in the vibrator sections 2 and 3, respectively mutually, and do not have deterioration of a RF property are obtained. [0023] the piezo-electric components concerning example] this invention besides [are not limited to said example, within the limits of the summary, can be boiled variously and can deform. About the configuration of a slot of dividing a closure substrate and a piezo electric crystal substrate, and a width-of-face size, it is arbitrary, and various things are adopted according to specification. [0024] Moreover, although said each example is performing all electrical installation of an internal electrode by the end face of a layered product, it is not necessarily limited to this and may perform electrical installation of an internal electrode using the connecting means of a through hole etc. [0025]

substrates which form oscillating space on both sides of a piezo electric crystal substrate and this piezo electric crystal according to [so that clearly] this invention Since the slot which divides one [said] closure substrate and said piezo electric crystal substrate was prepared and this slot was filled up with resin with a dielectric constant lower than a piezo electric crystal substrate and a closure substrate, the dielectric constant during I/O of piezo-electric components becomes small, and piezo-electric components with the small stray capacity generated between I/O can be obtained.

[0026] Moreover, the fall of the mechanical strength of the piezo-electric components by the slot can be compensated by the reinforcement member by joining a reinforcement member to the closure substrate divided by the slot, where said slot is straddled at least. Moreover, if a dielectric substrate is joined to the layered product which consists of a piezo electric crystal substrate and a closure substrate, the capacity for combining the two vibrator sections is securable in the capacitor section prepared in the dielectric. Therefore, it becomes unnecessary to prepare the capacitor section in a piezo electric crystal substrate, area of the part and a piezo electric crystal substrate can be made small, and the small piezo-electric components of a component-side product are obtained.

[Effect of the Invention] By the above explanation to the layered product which consisted of two closure

[0027] Furthermore, if the slot which divides a piezo electric crystal substrate and one closure substrate, respectively is established even in the closure substrate of another side, in case a division slot will be formed, a piezo electric crystal substrate can be certainly divided for one closure substrate, and compaction of working hours can be aimed at. Moreover, while joining a propagation-of-vibration prevention member to the layered product which consists of a piezo electric crystal substrate and a closure substrate through flexible adhesives and dividing said closure substrate and piezo electric crystal substrate The slot which reaches even said propagation-of-vibration prevention member is prepared between the two vibrator sections, and if the structure which fills up this slot with resin is adopted, vibration generated in one vibrator section will be interrupted by the resin and flexible adhesives which are filled up with a slot, and will not affect the vibrator section of another side. Consequently, the piezo-electric components of the structure where interference between the vibrator sections can be suppressed can be obtained.

[Translation done.]

* NOTICES *

Japan Patent Office is not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.*** shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] The assembly perspective diagram showing the 1st example of the piezo-electric components concerning this invention.

[Drawing 2] The plan of the piezo electric crystal substrate shown in drawing 1.

[Drawing 3] The perspective diagram for explaining a division slot formation production process.

[Drawing 4] The perspective diagram showing the appearance of piezo-electric components.

[Drawing 5] The vertical cross section of X-X' of drawing 4.

[Drawing 6] The electric representative circuit schematic of the piezo-electric components shown in drawing 5.

[Drawing 7] The assembly perspective diagram showing the 2nd example of the piezo-electric components concerning this invention.

[Drawing 8] The plan of the piezo electric crystal substrate shown in drawing 7.

[Drawing 9] The perspective diagram showing the appearance of piezo-electric components.

[Drawing 10] The vertical cross section of X-X' of drawing 9.

[Drawing 11] The vertical cross section showing the 3rd example of the piezo-electric components concerning this invention.

[Drawing 12] The assembly perspective diagram showing the 4th example of the piezo-electric components concerning this invention.

[Drawing 13] The plan of the piezo electric crystal substrate shown in drawing 12.

[Drawing 14] The assembly perspective diagram for explaining the assembly production process following drawing 12.

[Drawing 15] The perspective diagram showing the appearance of piezo-electric components.

[Drawing 16] The vertical cross section of X-X' of drawing 15.

[Drawing 17] The vertical cross section showing the 5th example of the piezo-electric components concerning this invention.

[Drawing 18] The perspective diagram showing the conventional example.

[Description of Notations]

1 -- Piezo electric crystal substrate

2 3 -- Vibrator section

4 5 -- Capacitor section

12 13 -- Closure substrate

17 -- Slot

18 -- Resin

31 -- Piezo electric crystal substrate

32 33 -- Vibrator section

36 -- Capacitor section

42 43 -- Closure substrate

47a, 47b -- Slot

- 48 -- Resin
- 60 -- Reinforcement substrate
- 71 -- Piezo electric crystal substrate
- 72 73 -- Vibrator section
- 82 83 -- Closure substrate
- 87 -- Slot
- 88 -- Resin
- 90 -- Dielectric substrate
- 91 -- Capacitor section
- 99a, 99b -- Slot
- 100 -- Slot
- 101 -- Resin
- 102 -- Flexible adhesives
- 103 -- Propagation-of-vibration prevention substrate

[Translation done.]

PAT-NO:

JP406006175A

DOCUMENT-IDENTIFIER:

JP 06006175 A

TITLE:

PIEZOELECTRIC COMPONENT

PUBN-DATE:

January 14, 1994

INVENTOR-INFORMATION:

NAME

IRIE, MAKOTO

ASSIGNEE-INFORMATION:

NAME

MURATA MFG CO LTD

COUNTRY

N/A

APPL-NO:

JP04161185

APPL-DATE:

June 19, 1992

INT-CL (IPC): H03H009/56, H03H009/02

US-CL-CURRENT: 333/191

ABSTRACT:

PURPOSE: To suppress stray capacitance generated between an input electrode and an output electrode and interference between vibrators.

CONSTITUTION: A vibrator section 2 and a capacitor section 4 are provided on

the input side of a piezoelectric substrate 1 and a vibrator section 3 and \boldsymbol{a}

capacitor section 5 are provided on the output side of the piezoelectric

substrate 1. Then the piezoelectric substrate 1 and sealing substrates $12,\ 13$

form a laminator 16 provided with an enclosed vibration space. After a groove

17 completely separating the piezoelectric substrate 1 and the sealing substrate 12 is formed among the vibrator section 2, the capacitor section 4,

the vibrator section 3, the capacitor section 5 in the laminator, the groove 17

is filled by a resin 18 whose dielectric constant is lower than that of the substrates 1, 12.

COPYRIGHT: (C)1994, JPO&Japio

(19)日本国特許庁(JP)

(12) 公開特許公報(A)

(11)特許出願公開番号

特開平6-6175

(43)公開日 平成6年(1994)1月14日

(51)Int.Cl.⁵

識別記号

庁内整理番号

FΙ

技術表示箇所

H03H 9/56 9/02 7259 - 5 J

8221-5 J

審査請求 未請求 請求項の数7(全 7 頁)

(21)出願番号

特願平4-161185

(22)出顧日

平成 4年(1992) 6月19日

(71)出願人 000006231

株式会社村田製作所

京都府長岡京市天神二丁目26番10号

(72)発明者 入江 誠

京都府長岡京市天神二丁目26番10号 株式

会社村田製作所内。

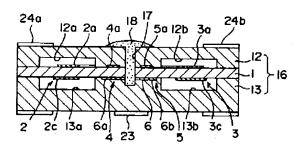
(74)代理人 弁理士 森下 武一

(54)【発明の名称】 圧電部品

(57)【要約】

【目的】 入力電極と出力電極の間に発生する浮遊容量 や振動子部相互間の干渉を抑えることができる構造の圧 電部品を得る。

【構成】 振動子部2及びコンデンサ部4を圧電体基板 1の入力側に設け、振動子部3及びコンデンサ部5を圧 電体基板1の出力側に設ける。そして、圧電体基板1と 封止基板12,13とで密閉された振動空間を備えた積 層体16を構成する。この積層体の振動子部2及びコン デンサ部4と、振動子部3及びコンデンサ部5との間 に、圧電体基板1及び封止基板12を完全に分割する溝 17を形成した後、この溝17を基板1,12より誘電 率の低い樹脂18にて充填する。



【特許請求の範囲】

【請求項1】 二つの振動子部と二つのコンデンサ部を 少なくとも有し、前記一方の振動子部及びコンデンサ部 を基板の一方の側に設け、前記他方の振動子部及びコン デンサ部を基板の他方の側に設けた圧電体基板と、

前記圧電体基板を挟んで振動空間を形成する二つの封止 基板とを備え、

前記圧電体基板と前記一方の封止基板をそれぞれ分割す る溝を、前記一方の振動子部及びコンデンサ部と、前記 他方の振動子部及びコンデンサ部との間に設け、この溝 10 に樹脂を充填したこと、

を特徴とする圧電部品。

【請求項2】 二つの振動子部とこの二つの振動子部の 間に配設された一つのコンデンサ部とを少なくとも有し た圧電体基板と、

前記圧電体基板を挟んで振動空間を形成する二つの封止 基板とを備え、

前記圧電体基板と前記一方の封止基板をそれぞれ分割す る溝を、前記コンデンサ部と前記一方の振動子部の間、 及び前記コンデンサ部と前記他方の振動子部の間に設 け、この溝に樹脂を充填したこと、

を特徴とする圧電部品。

【請求項3】 溝によって分割された封止基板に、少な くとも前記溝に跨った状態で補強部材が接合しているこ とを特徴とする請求項1又は請求項2記載の圧電部品。

【請求項4】 少なくとも二つの振動子部を有する圧電 体基板と、

前記圧電体基板を挟んで振動空間を形成する二つの封止 基板と、

少なくとも一つのコンデンサ部を有した誘電体基板とを 30 備え、

前記圧電体基板と前記一方の封止基板をそれぞれ分割す る溝を、前記二つの振動子部の間に設けると共にこの溝 に樹脂を充填し、前記誘電体基板を分割する溝を前記誘 電体基板の入力側と出力側の間に設けたこと、

を特徴とする圧電部品。

【請求項5】 誘電体基板を分割する溝が、前記誘電体 基板に接する封止基板にまで設けられていることを特徴 とする請求項4記載の圧電部品。

【請求項6】 一方の封止基板と圧電体基板をそれぞれ 40 分割する溝を、他方の封止基板にまで設けたことを特徴 とする請求項1又は請求項2又は請求項3又は請求項4 又は請求項5記載の圧電部品。

【請求項7】 二つの振動子部と一つのコンデンサ部を 少なくとも有した圧電体基板と、

前記圧電体基板を挟んで振動空間を形成する二つの封止 基板と、

前記一方の封止基板にフレキシブル接着剤を介して接合 した振動伝搬防止部材とを備え、

前記二つの封止基板と前記圧電体基板をそれぞれ分割す 50 【0007】さらに、圧電体基板と封止基板からなる積

ると共に前記振動伝搬防止部材にまで達する溝を前記振 動子部の間に設け、この溝に樹脂を充填したこと、を特 徴とする圧電部品。

【発明の詳細な説明】

[0001]

(2)

【産業上の利用分野】本発明は、発振回路、フィルタ回 路等に使用される圧電部品に関する。

[0002]

【従来の技術と課題】従来より、この種の圧電部品とし ては、図18に示す積層タイプのものがある。この圧電 部品は、二つの振動子部とこの二つの振動子部を結合す るためのコンデンサ部を有する圧電体基板120と、こ の圧電体基板120を挟んで振動空間を形成する二つの 封止基板121、122とで積層体を構成している。こ の積層体の両端部には入力電極125、出力電極126 が設けられ、中央部にはグランド電極127が設けられ ている。

【0003】ところが、この圧電部品をさらに小型化す ると、入力電極125と出力電極126の間隔が狭くな るため、入力電極125と出力電極126との間に大き な浮遊容量が発生した。また、これとは別に、一方の振 動子部で生じた振動が、封止基板121,122を伝搬 して他方の振動子部に干渉することにより、高周波特性 が劣化するという問題もあった。

【0004】そこで、本発明の課題は、入力電極と出力 電極の間に発生する浮遊容量や振動子部相互間の干渉を 抑えることができる構造の圧電部品を提供することにあ る。

【0005】

【課題を解決するための手段と作用】以上の課題を解決 するため、本発明に係る圧電部品は、二つの振動子部と この二つの振動子部を結合するためのコンデンサ部を有 する圧電体基板と、この圧電体基板を挟んで振動空間を 形成する二つの封止基板とを備え、前記圧電体基板と前 記一方の封止基板をそれぞれ分割する溝を前記二つの振 動子部の間に設けた。この溝には、圧電体基板や封止基 板より低い誘電率の樹脂、例えばエポキシ系樹脂等が充 填される。従って、溝を設けた圧電体基板及び封止基板 全体の誘電率が小さくなるため、圧電部品の入出力間に 発生する浮遊容量が小さくなる。

【0006】また、溝によって分割された封止基板に、 少なくとも前記溝に跨った状態で補強部材を接合させれ ば、溝を設けたことによる圧電部品の機械的強度の低下 が補償される。また、誘電体基板を圧電体基板と封止基 板からなる積層体に接合し、二つの振動子部を結合する ための容量を誘電体基板に設けたコンデンサ部にて確保 してもよい。この場合、誘電体基板に誘電体基板を分割 する溝を設けることにより、圧電部品の入出力間の浮遊 容量が抑えられる。

層体に、圧電体基板と一方の封止基板を分割する溝を形成する場合、この分割溝を他方の封止基板にまで設けるようにすれば、前記圧電体基板と前記一方の封止基板は確実に分割される。また、振動伝搬防止部材をフレキシブル接着剤、例えばゴム系の接着剤を介して圧電体基板と封止基板からなる積層体に接合し、前記圧電体基板と前記封止基板を分割すると共に前記振動伝搬防止部材にまで達する溝を二つの振動子部の間に設け、この溝に樹脂を充填する構造を採用すれば、一方の振動子部に生じる振動は溝を充填する樹脂とフレキシブル接着剤によっ10で遮られ、他方の振動子部に影響を及ばさなくですむ。【0008】

【実施例】以下、本発明に係る圧電部品の実施例を添付 図面を参照して説明する。各実施例において同一部品及 び同一部分には同じ符号を付した。

[第1実施例、図1~図6]第1実施例の圧電部品は、二つの圧電振動子と二つのコンデンサを内蔵したものであり、そのうちの一つの圧電振動子とコンデンサを圧電体基板の入力側に設け、他の圧電振動子とコンデンサを圧電体基板の出力側に設けたものである。図1に示すように、圧電部品は、圧電体基板1と、この圧電体基板1を挟んで振動空間を形成する封止基板12,13とで構成されている。

【0009】圧電体基板1は、PZT等のセラミック材からなる。圧電体基板1の表面には、振動電極2a,2b、振動電極3a,3b、容量電極4a、容量電極5a、電極2b,3bにそれぞれ接続された入力側引出し電極7及び出力側引出し電極8、補助電極9a,9bが設けられている。さらに、基板1の裏面には、図2に示すように、振動電極2c,振動電極3c及び後述の分割30溝17によって最終的には容量電極6aと容量電極6bに分割されるべき容量電極6が所定の位置に設けられている。振動子部2,3はそれぞれ振動電極2a~2c,3a~3cにて構成され、コンデンサ部4,5はそれぞれ容量電極4a,6a、5a,6bにて構成されている。

【0010】絶縁性封止基板12,13は、一方の面に振動空間形成用凹部12a,12b(図4参照)、13a,13bを配設している。次に、以上の構成品1,12,13の組立て手順について説明する。まず、図3に示すように圧電体基板1の上下に封止基板12,13を工ポキシ系の接着剤等を介して固着し、密閉された振動空間を有する積層体16とする。この積層体16に圧電体基板1及び封止基板12を2分割する溝17を形成する。このとき、圧電体基板1及び封止基板12を完全に分割するため、製造公差等も考慮して溝17を封止基板13にまで設けるべく、溝17の深さが設定される。ただし、基板1,12を完全に分割するという条件を満足すれば、必ずしも基板13に溝17を設ける必要はない。容量電極6は溝17によって分割され、容量電極6

aと容量電極6bとされる。従って、この溝17は、振動子部2及びコンデンサ部4と振動子部3及びコンデンサ部5との間に設けられることとなる。

【0011】次に、図4に示すように、溝17に樹脂18を充填する。樹脂18の材料としては、圧電体基板1や封止基板12,13より低い誘電率のもの、例えばエポキシ系樹脂等が用いられる。樹脂18は溝17の開口部に広面積に盛り上げられた状態で形成されている。溝17を形成したことによる圧電部品の機械的強度の低下を樹脂18を盛り上げることによって少なくするためである。

【0012】次に、図5に示すように、積層体16の両端部にスパッタや蒸着等の薄膜形成手段にて入力電極20、出力電極21及び補助電極24a,24bをそれぞれ形成すると共に、積層体16の手前側端面に中継電極22、奥側端面にグランド電極23を形成する。そして、入力電極20は引出し電極7に接続され、出力電極21は引出し電極8に接続され、中継電極22は容量電極4a,5aに接続され、グランド電極23は容量電極6a,6bに接続される。図6に圧電部品の電気等価回路図を示す。

【0013】こうして得られた圧電部品は、入力電極2 0と出力電極21の間に誘電率の低い樹脂18にて充填された溝17が形成されているので、入力電極20と出力電極21の間の誘電率が小さくなり、入出力電極20 -21間に発生する浮遊容量が小さいものとなる。

[第2実施例、図7~図10]第2実施例の圧電部品 は、二つの圧電振動子と一つのコンデンサを備えたもの である。図7に示すように、圧電体基板31と、この圧 電体基板31を挟んで振動空間を形成する封止基板4 2,43とで構成されている。圧電体基板31の表面に は、振動電極32a,32b、振動電極33a,33 b、電極32a,33aにそれぞれ接続された入力側引 出し電極37、出力側引出し電極38、電極32b,3 3bにそれぞれ接続された中継引出し電極34a,34 b及び容量電極36aが設けられている。さらに、基板 31の裏面には、図8に示すように、振動電極32c、 振動電極33c、電極32c,33cにそれぞれ接続さ れたグランド側引出し電極35a,35b及び容量電極 36 bが所定の位置に設けられている。振動子部32, 33はそれぞれ振動電極32a~32c, 33a~33 cにて構成され、コンデンサ部36は容量電極36a, 36bにて構成されている。この圧電体基板1は、コン デンサ部36の両側部分に設けられる後述の2本の分割 溝47a,47bによって分割されることとなる。

【0014】絶縁性封止基板42,43は一方の面に振動空間形成用凹部42a,42b(図9参照)、43a,43bを配設している。図9に示すように、これらの構成品31,42,43は積み重ねられて固着され、50 密閉された振動空間を有する積層体46とされる。この

積層体46には圧電体基板31及び封止基板42を3分 割する2本の溝47a及び47bが、それぞれ振動子部 32とコンデンサ部36の間及び振動子部33とコンデ ンサ部36の間に形成される。この溝47a、47bは 誘電率の低い樹脂48が充填されており、溝47a,4 7 bの開口部は樹脂48が広面積に盛り上げられてい

【0015】次に、図10に示すように、積層体46の 両端部に薄膜形成手段にて入力電極50、出力電極51 及び補助電極54a,54bをそれぞれ形成すると共 に、積層体46の手前側端面に中継電極52、奥側端面 にグランド電極53を形成する。そして、入力電極50 は引出し電極37に接続され、出力電極51は引出し電 極38に接続され、中継電極52は容量電極36a及び 中継電極34a,34bに接続され、グランド電極53 は容量電極36b及び引出し電極35a,35bに接続 される。

【0016】こうして得られた圧電部品は、前記第1実 施例の圧電部品と同様の作用、効果を奏する。

[第3実施例、図11] 図11に示すように、第3実施 20 例の圧電部品は、前記第1実施例において説明した圧電 体基板1と封止基板12,13とで構成される積層体1 6に、エポキシ系等の接着剤にて補強基板60を固着し たものである。溝17は圧電体基板1と封止基板12を 2分割し、封止基板13にまで設けられている。溝17 は誘電率の低い樹脂18にて充填されている。圧電部品 の両端部には入力電極61、出力電極62が設けられ、 中央部にはグランド電極63が設けられている。

【0017】以上の構成からなる圧電部品は前記第1実 施例の圧電部品と同様の作用、効果を奏すると共に、補 30 強基板60によって満17を形成したことによる圧電部 品の機械的強度の低下を充分補償することができる構造 となっている。

[第4実施例、図12~図16]第4実施例の圧電部品 は、二つの圧電振動子と一つのコンデンサを内蔵したも のである。図12に示すように、圧電体基板71と、こ の圧電体基板を挟んで振動空間を形成する封止基板8 2,83と、誘電体基板88(図14参照)とで構成さ れている。圧電体基板71の表面には振動電極72a, 72b、振動電極73a, 73b、電極72a, 73a にそれぞれ接続された入力側引出し電極75及び出力側 引出し電極76、電極72b, 73bにそれぞれ接続さ れた中継引出し電極77a, 77b、補助電極78a, 78bが設けられている。さらに、基板71の裏面に は、図13に示すように、振動電極72c、振動電極7 3c、電極72c,73cにそれぞれ接続されたグラン ド側引出し電極79a, 79bが設けられている。振動 子部72,73はそれぞれ振動電極72a~72c,7 3a~73cにて構成されている。この圧電体基板71

の分割溝87によって分割されることになる。封止基板 82,83は一方の面には振動空間形成用凹部82a, 82b (図16参照)、83a,83bが配設されてい

【0018】図14に示すように、これらの構成品7 1,82,83は積み重ねられて固着され、密閉された 振動空間を有する積層体85とされる。 この積層体85 に圧電体基板71及び封止基板82を2分割する溝87 が、振動子部72と振動子部73の間に形成される。こ 10 の溝87は誘電率の低い樹脂88にて充填されている。 この積層体85の上面に誘電体基板90が接着剤を介し て固着する。誘電体基板90の表面には容量電極91 a、下地電極92a, 92b, 92c, 92dが設けら れている。誘電体基板90の裏面には容量電極91bが 設けられている。コンデンサ部91は容量電極91a, 916にて構成されている。

【0019】次に、図15に示すように、圧電部品の両 端部に薄膜形成手段にて入力電極94、出力電極95及 び補助電極98a, 98bをそれぞれ形成すると共に、 圧電部品の手前側端面部に中継電極96、奥側端面部に グランド電極97を形成する。入力電極94は入力側引 出し電極75に接続され、出力電極95は出力側引出し 電極76に接続され、中継電極96は中継引出し電極7 7a,77bに接続され、グランド電極97はグランド 引出し電極79a,79bに接続される。

【0020】さらに、入力電極94と出力電極95間の 浮遊容量を抑えるために、入力電極94とグランド電極 97の間及び出力電極95とグランド電極97の間に幅 の広い溝99a, 99bを形成する。この溝99a, 9 9 b は誘電体基板 9 0 を完全に 3 分割するように、製造 公差等も考慮して封止基板82にまで設けられている。 但し、基板90を完全に分割するという条件を満足すれ ば、必ずしも基板82に溝99a,99bを設ける必要 はない。また、溝99a,99bのいずれか一方の溝だ けを設けるものであってもよい。

【0021】図16は得られた圧電部品の垂直断面図で ある。容量電極91aと91bの間に容量が発生する。 この圧電部品は前記第1実施例の圧電部品と同様の作 用、効果を奏する。

[第5実施例、図17]図17に示すように、第5実施 例の圧電部品は、前記第1実施例において説明した圧電 体基板1と封止基板12,13とで構成される積層体1 6に、フレキシブル接着剤(柔軟性を有する接着剤)1 02にて振動伝搬防止基板103を接着したものであ る。フレキシブル接着剤102の材料としては、ゴム系 の樹脂接着剤等が用いられる。溝100は圧電体基板1 と封止基板12,13を2分割し、振動伝搬防止基板1 03にまで設けられている。溝100は誘電率の低い樹 脂101にて充填されている。圧電部品の両端部には入 は、振動子部72と振動子部73の間に設けられる後述 50 力電極104及び出力電極105が設けられている。

【0022】以上の構成において、振動子部2及び3にてそれぞれ発生した振動は一部封止基板12,13に伝搬される。しかし、封止基板12に伝搬した振動は溝100に充填された樹脂101によって遮られ、封止基板13に伝搬した振動はフレキシブル接着剤102によって遮られる。従って、振動子部2及び3にてそれぞれ発生した振動は、相互に干渉し合うことがなく、高周波特性の劣化のない圧電部品が得られる。

【0023】[他の実施例] 本発明に係る圧電部品は前記実施例に限定されるものではなく、その要旨の範囲内 10で種々に変形することができる。封止基板や圧電体基板を分割する溝の形状、幅寸法等については任意であり、仕様に合わせて種々のものが採用される。

【0024】また、前記各実施例は内部電極の電気的接続を全て積層体の端面で行っているが、必ずしもこれに限定されるものではなく、スルーホール等の接続手段を用いて内部電極の電気的接続を行ってもよい。

[0025]

【発明の効果】以上の説明で明らかなように、本発明によれば、圧電体基板とこの圧電体を挟んで振動空間を形 20 成する二つの封止基板とで構成された積層体に、前記一方の封止基板と前記圧電体基板を分割する溝を設け、この溝に圧電体基板や封止基板より誘電率の低い樹脂を充填したので、圧電部品の入出力間の誘電率が小さくなり、入出力間に発生する浮遊容量が小さい圧電部品を得ることができる。

【0026】また、溝によって分割された封止基板に、少なくとも前記溝に跨った状態で補強部材を接合させることにより、溝による圧電部品の機械的強度の低下を補強部材によって補償することができる。また、誘電体基30板を圧電体基板と封止基板からなる積層体に接合すれば、二つの振動子部を結合するための容量を誘電体に設けたコンデンサ部にて確保することができる。従って、圧電体基板にコンデンサ部を設ける必要がなくなり、その分、圧電体基板の面積を小さくすることができ、実装面積の小さい圧電部品が得られる。

【0027】さらに、圧電体基板と一方の封止基板をそれぞれ分割する溝を他方の封止基板にまで設けるようにすれば、分割溝を形成する際、一方の封止基板を圧電体基板を確実に分割することができ、作業時間の短縮が図40れる。また、振動伝搬防止部材をフレキシブル接着剤を介して圧電体基板と封止基板からなる積層体に接合し、前記封止基板と圧電体基板を分割すると共に、前記振動伝搬防止部材にまで達する溝を二つの振動子部の間に設け、この溝に樹脂を充填する構造を採用すれば、一方の振動子部に発生した振動は溝を充填する樹脂とフレキシブル接着剤によって遮られ、他方の振動子部に影響を及ばさない。この結果、振動子部相互間の干渉を抑えることができる構造の圧電部品を得ることができる。

【図面の簡単な説明】

【図1】本発明に係る圧電部品の第1実施例を示す組立て斜視図。

【図2】図1に示されている圧電体基板の平面図。

【図3】分割溝形成工程を説明するための斜視図。

【図4】圧電部品の外観を示す斜視図。

【図5】図4のX-X'の垂直断面図。

【図6】図5に示した圧電部品の電気等価回路図。

【図7】本発明に係る圧電部品の第2実施例を示す組立て斜視図。

10 【図8】図7に示されている圧電体基板の平面図。

【図9】圧電部品の外観を示す斜視図。

【図10】図9のX-X'の垂直断面図。

【図11】本発明に係る圧電部品の第3実施例を示す垂 直断面図。

【図12】本発明に係る圧電部品の第4実施例を示す組立て斜視図。

【図13】図12に示されている圧電体基板の平面図。

【図14】図12に続く組立て工程を説明するための組立て斜視図。

20 【図15】圧電部品の外観を示す斜視図。

【図16】図15のX-X'の垂直断面図。

【図17】本発明に係る圧電部品の第5実施例を示す垂 直断面図。

【図18】従来例を示す斜視図。

【符号の説明】

1…圧電体基板

2,3…振動子部

4,5…コンデンサ部

12, 13…封止基板

30 17…溝

18…樹脂

31…圧電体基板

32,33…振動子部

36…コンデンサ部

42,43…封止基板

47a, 47b…溝

48…樹脂

60…補強基板

71…圧電体基板

72.73…振動子部

82,83…封止基板

87…溝

88…樹脂

90…誘電体基板

91…コンデンサ部

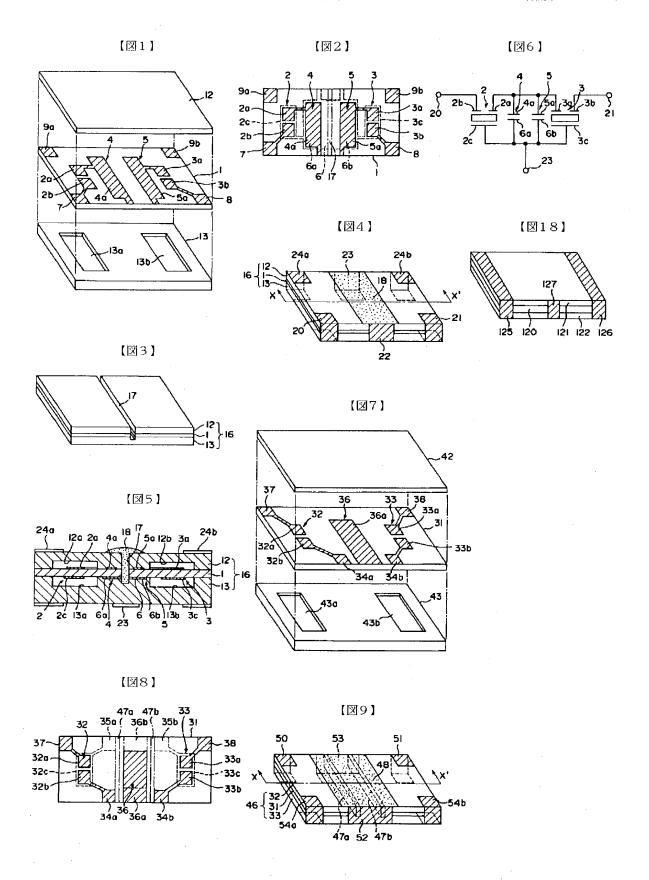
99a, 99b…溝

100…溝

101…樹脂

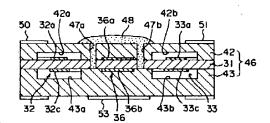
102…フレキシブル接着剤

50 103…振動伝搬防止基板

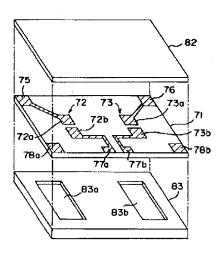


01/30/2004, EAST Version: 1.4.1

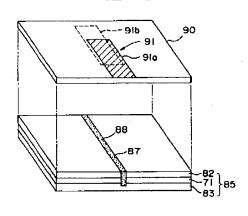
【図10】



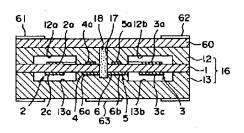
【図12】



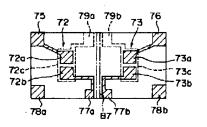
【図14】



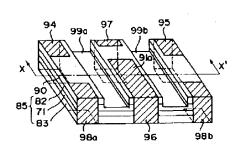
【図11】



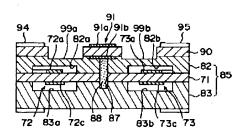
【図13】



【図15】



【図16】



【図17】

